

## ARM - Xilinx Advanced SoC Lab-in-a-Box Workshop

### Agenda

Time	Topic	Lab Folder
8:30 – 9:00 AM	Registration	
9:00 – 9:30 AM	AUP Introduction XUP Introduction	
9:30 – 10:00 AM	Xilinx <sup>®</sup> Zynq <sup>™</sup> -7000 All Programmable SoC Xilinx Vivado <sup>™</sup> Design Flow	
10:00 – 10:15 AM	Break	
10:15 – 10:45 AM	Introduction to ARMv7 Architecture and ARM <sup>®</sup> Cortex <sup>®</sup> -A9 Processor ARM DS-5 Development Tool	
10:45 – 11:30 AM	Lab1: Simple Program on Cortex-A9 FVP using DS-5 – String Capitalization (add GDB sdk deug)	
11:30 – 1:30 PM	Lunch	
1:30 – 2:30 PM	Lab2: Basic Zynq-based SoC Design using Vivado – Blinking LED	
2:30 – 3:30 PM	Lab3: Integrating Display Peripherals to Zynq SoC using Vivado – Image Mirroring	
3:30 – 3:45 PM	Break	
3:45 – 4:45	Lab4: Implementing Image Processing Algorithm on Cortex-A9 using DS-5 – Edge Detection Application	
4:45 – 5:00 PM	Conclusion, Call for Action, ARM-Xilinx Lab- in-a-Box	